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PATENT APPLICATION TRANSMITTAL LETTER

To the Assistant Commissioner of Patents
Washington, D.C. 20231

Transmitted herewith for filing under 35 U.S.C. 111(a) and 37 CFR 1.53(b) is the patent application of

___ **Jorg Borowski and Klaus Meyer**

entitled___ **DIGITAL CORRECTION METHOD AND SYSTEM**

Enclosed are:

X   A specification consisting of: an **18** page disclosure including **4** pages of claims, and abstract;

X   Three (3) sets of informal drawings, each set consisting of 3 sheets;

X   Executed Declaration of the Inventors; and

X   Assignment by the Inventors.

CLAIMS AS FILED

CLAIMS	(1) FOR	(2) NUMBER FILED	(3) NUMBER EXTRA	(4) RATE	(5) CALCULATIONS
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	INDEPENDENT CLAIMS (37 CFR 1 16(b))	3 - 3=	0	X $ 78.00 =	0.00
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DIGITAL CORRECTION METHOD AND SYSTEM

BACKGROUND OF THE INVENTION

5

The present invention relates to a method and system for correcting an offset in a digital system. In particular, such a correction may be required in a digital communication system in which a frequency

10 offset is present.

Digital communication systems are widely used for transmitting and receiving information. Examples of such digital communication systems include mobile

15 telephone systems, for example the European GSM (Global System for Mobile Communications) system and the American D-AMPS system. Digital communication systems are also being developed for television transmissions.

20

Within the telecommunications community, there is a desire for a single telecommunication protocol to be introduced enabling the use of a single mobile telephone anywhere in the world. Whilst such a

25 system is not yet in operation, it is apparent that the basis for such a system is likely to be one which uses a spread spectrum signal, in the form of a code division multiple access (CDMA) system.

30 In a CDMA system, a digital data signal is modulated by a second digital signal, the second signal generally having a significantly higher switching rate. Spread spectrum communication systems were originally developed for military and satellite

35 applications but have since been developed for

commercial mobile telephone applications. In the United States of America, the Telecommunications Industry Association has introduced the IS-95 CDMA standard for cellular telecommunications.

At a receiver, a radio receiver converts the received radio signal into an electrical signal. This is then mixed with an oscillator signal to recover the analog spread spectrum signal. An analog to digital converter samples the analog signal to obtain a digital signal, which may be an oversampled signal. This digital signal is despread by correlating the signal with the same pseudo-random code used in the generation of the digital spread spectrum signal to recreate the data signal.

In a CDMA communication system, there is a general requirement to perform automatic frequency correction at a receiver to take into account any systematic or instantaneous frequency variations, in particular transmit and receive oscillator frequency offsets. This is particularly the case in mobile systems, where the motion of the receiver with respect to the transmitter may result in a Doppler shift of the signal.

A variety of techniques have been employed in the prior art to provide automatic frequency correction in CDMA systems. One prior art technique is to perform frequency correction at the input to the receiver. In a CDMA receiver, the input to the receiver has the highest sampling rate and hence this solution demands a very high computational requirement. An alternative prior art solution is to perform frequency error detection and correction after the signal has been de-spread, at the symbol

level. Such a solution is however severely limited in the range of frequency correction.

European patent application EP-A-0 762 666 describes a CDMA mobile communication system incorporating means for performing a phase error correction in which a transmitted pilot signal is despread and used to determine phase correction signals which are then fed back to a voltage controlled oscillator. In an alternative embodiment, frequency correction is performed by deriving phase correction signals from phase rotation changes of data with no use of pilot signals for frequency control.

SUMMARY OF THE INVENTION

The present invention provides a method of providing frequency correction for a spread spectrum communication receiver, said receiver being arranged to despread a code-spread signal having a first data rate to provide at least one despread data signal having a second, lower data rate, wherein said method comprises the steps of:

i) determining a frequency offset by processing successive samples of said despread data signal;

ii) generating a correction sequence from said determined frequency offset; and

iii) combining said code-spread signal having said first data rate with said correction sequence obtained from said despread data signal having said second, lower data rate to correct the determined frequency offset.

By measuring a frequency offset at a relatively low signal data rate and combining a frequency correction with a data signal at a relatively higher rate, it is

possible to reduce the amount of calculation required to determine the offset whilst allowing a relatively large range of frequency correction.

5 Preferably, the method includes a filtering step wherein any noise component in determined frequency offsets is reduced. This filtering may be performed by using a weighted average of previous measurements.

10 The determination of the frequency offset may be performed by calculating the mathematical argument of a complex sample of the despread data signal multiplied by the complex conjugate of the immediately preceding sample. The correction

15 sequence may be combined with the code-spread signal including a frequency error by the multiplication of a complex correction factor therewith. In order to arrive at a correction factor for combination with the code-spread signal, a linear interpolation from

20 the despread signal offset may be beneficially used.

In a further aspect of the invention, there is provided a spread spectrum communication system comprising a plurality of receivers for receiving

25 transmitted signals, wherein each receiver comprises:
an RF signal receiver for generating an analog signal from a received RF signal;
an analog to digital converter for converting said analog signal into a digital signal;

30 a digital signal despreader for processing a code-spread signal having a first data rate to obtain a despread digital signal having a second data rate, said second data rate being lower than said first data rate; and

35 a frequency corrector,

wherein said frequency corrector comprises a feedback loop including a frequency offset detector for obtaining a measure of a frequency offset from said despread digital signal and a frequency correction
5 generator for generating a frequency correction and a combiner for combining said frequency correction with said code-spread signal to correct said frequency offset.

10 In a still further aspect of the invention, there is provided a receiver for a spread spectrum communication system comprising:
an RF signal receiver for generating an analog signal from a received RF signal;
15 an analog to digital converter for converting said analog signal into a digital signal;
a digital signal despreader for processing a code-spread signal having a first data rate to obtain a despread digital signal having a second data rate,
20 said second data rate being lower than said first data rate; and
a frequency corrector,
wherein said frequency corrector comprises a feedback loop including a frequency offset detector for
25 obtaining a measure of a frequency offset from said despread digital signal and a frequency correction generator for generating a frequency correction and a combiner for combining said frequency correction with said code-spread signal to correct said frequency
30 offset.

The spread spectrum communication system may be a code division multiple access system, and in particular a telephone communication system. In a
35 mobile communication system, T_x and R_x oscillator frequency offsets and the relative movement of a

transmitter and a receiver requires the necessity to perform frequency correction. Another example of a communication system in which the invention may be incorporated is a wireless local loop link.

5

BRIEF DESCRIPTION OF THE DRAWINGS

An embodiment of the present invention will now be described, by way of example only, with reference to
10   the accompanying drawings in which:

Fig. 1   shows a schematic diagram of a generalized CDMA communication system;

15   Fig. 2   shows a schematic representation of a CDMA communication system receiver incorporating frequency correction; and

Fig. 3   shows the Fig. 2 frequency correction
20   arrangement in greater detail.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

Referring now to figure 1, there is shown a CDMA
25   communication system, indicated generally by 10. The communication system 10 comprises a transmitter 12 and a receiver 14. Of course, for a two way communication system each communication unit will have transmit and receive capabilities. In a CDMA
30   mobile telephone system a base station will be capable of transmitting data to and receiving data from a plurality of remote units whilst each remote unit will receive messages from and transmit to, in general, only a single base station at any one time.

35

At the transmitter 12, a data signal to be transmitted 16, comprising a plurality of bits, is modulated by a chip code 18 in a modulation circuit 20. In the following, it is assumed that the data
5 signal to be transmitted is transmitted according to a quaternary phase shift keying (QPSK) scheme, in which in-phase (I) and quadrature (Q) signals are modulated separately. The resulting digital spread spectrum signals are converted into an analog spread
10 spectrum signal by a filter 22 and then mixed with a carrier frequency 24 before transmission over a channel 26.

At the receiver 14, a received RF signal has the
15 carrier frequency removed and is then demodulated into I and Q signals. These are sampled and converted into digital signals by an analog to digital converter 28. These digital signals are despread by a correlator 30 in combination with a
20 despreading code 31. Generally, the de-spreading code 31 is identical to the chip code 18. Following despreading the resulting data signals may be processed in an error correction circuit 32.

25 The data signal 16 has a data rate which is referred to as the symbol rate and which may typically be 128 kbit/s. The chip code 18 might typically have a data rate, known as the chip rate of 4096 chips/s. These two figures indicate a spreading factor of 32, this
30 being the number of chips/bit.

Figure 2 shows a CDMA communication receiver 50 for incorporation in the system 10 having automatic frequency control which measures an offset at the
35 symbol rate level and implements a frequency correction at the chip rate level. In the following

description, it is assumed that quadrature
demodulation was used for final down conversion of
the analog signal. Hence, all operations in the
signal processing chain have to be performed on both
5   in-phase and quadrature components.

A data modulated and CDMA-code spread complex base-
band signal 52 is passed through an analog filter 54
to an analog to digital converter 56. The analog to
10   digital converter 56 receives a clock input from a
clock 58 at a frequency which is four times that of
the chip rate. The analog signal is thus sampled at
a rate which is four times the chip rate, or 16.4
Msamples/s. The resulting oversampled signal 60
15   passes through a conventional noise and pulse shaping
square root raised cosine complex filter 62. Timing
correction is performed by a timing circuitry 64. The
resulting data stream is thereby synchronized with
respect to frame, symbol and chip time at more than
20   the chip rate. The next stage is for the data stream
to be down-converted by a down-converter 66 to the
chip rate to provide one complex sample pair per
chip, taken at the optimum sampling instant.

25   The above standard signal processing operations.
After down converting to the chip rate, the data
stream is passed to a despreading stage which
includes the automatic frequency control of the
invention. After time alignment, the resulting time-
30   aligned complex samples $Z_{chip,offs}(k)$ before frequency
correction have a frequency offset f_{offs} to be
corrected. This correction is performed by
multiplying $Z_{chip,offs}(k)$ by the complex conjugate of a
determined correction envelope $Z_{offs}(k)$.

35

Despreading is performed by a despreader 68. In the despreader 68, two parallel code-length tap FIR-structure matched filters (not shown) are employed, one for each of the in-phase and quadrature branches.

5   The same real valued CDMA-code vector is applied as filter coefficients to both the in-phase and quadrature branch. Since timing is known, multiply and add operations are executed at the known symbol sampling instants only, i.e. once per symbol for

10   every tap. As a result of the complex correlation despreading operation performed by the despreader 68, a data stream $Z_{sym}(n)$ at the symbol rate is obtained.

Frequency error detection is performed by a detector

15   70. The detector determines a frequency error and feeds this value to a numerically controlled oscillator 72 which produces a offset correction which is multiplied with the samples to be corrected by a multiplier 74.

20

After despreading, the resultant symbol stream is passed to a carrier phase corrector 76 and thence a coherent demodulator 78. Error correction is provided by a Viterbi convolutional decoder 80.

25   Where appropriate, the resulting processed signal is then processed to arrange the information into individual channels and if these correspond to a voice transmission they may be converted into an analog voice signal.

30

Referring now to Figure 3, the automatic frequency correction stage of the receiver 50 is shown in greater detail.

At a sampling instant, n, the symbol samples may be expressed by the following equation

$$\underline{Z}_{sym}(n) = A(n) \cdot \exp\left\{ j\left[\varphi_{mod}(n) + \varphi_{offs}(n) + \varphi_{noise}(n) + \varphi_{o}(n) \right] \right\}$$

At each sampling instant n, the symbol samples contain information concerning a modulation phase shift $\varphi_{mod}(n)$ and the oscillator frequency offset through a linearly changing phase offset $\varphi_{offs}(n)$. These phase shifts are distorted by a noise component which is randomly changing $\varphi_{noise}(n)$ and there is also a constant initial phase value $\varphi_0(n)$.

The frequency error detector 70 determines $f_{offs}(n)$ from incoming complex signal samples $Z_{sym}(n)$ using the phase difference $\varphi_{offs}(n)$ - $\varphi_{offs}(n-1)$ between two consecutive symbol samples $Z_{sym}(n)$ and $Z_{sym}(n-1)$. The frequency offset $f_{offs}(n)$ is obtained by performing the following complex multiplication and argument operation:

$$\varphi_{offs}(n) - \varphi_{offs}(n-1) + \Delta\varphi_{mod}(n) + \Delta\varphi_{noise}(n) = \arg\left\{ \underline{Z}_{sym}(n) \cdot \underline{Z}_{sym}^{*}(n-1) \right\}$$

where ( )* denotes a complex conjugate operation.

The differential modulation phase $\Delta\varphi_{mod}(n)$ contains one out of a set of m known values and is removed by rotating the constellation point around zero origin so that the resulting vector has an angle value less than half the smallest differential modulation phase. For (D)QPSK modulation this results in

$$\Delta\varphi_{offs}(n) + \Delta\varphi_{noise}(n) = \varphi_{offs}(n) - \varphi_{offs}(n-1) \quad + \quad \Delta\varphi_{noise}(n)$$

$$= \left[\varphi_{offs}(n) - \varphi_{offs}(n-1) + \Delta\varphi_{mod}(n) + \Delta\varphi_{noise}(n)\right] \bmod ulo \frac{\pi}{2}$$

with the resulting phase difference falling into the interval $-\pi/4 \le \Delta\phi_{offs} + \Delta\phi_{noise} < \pi/4$ so that a frequency offset

$$f_{offs}(n) + f_{noise}(n) = \frac{\Delta\varphi_{offs}(n) \quad + \quad \Delta\varphi_{noise}(n)}{T_{sym}}$$

can be captured in the range

$$-\frac{\pi}{4\,T_{sym}} \le f_{offs} + f_{noise} < \frac{\pi}{4\,T_{sym}}$$

with T_{sym} being the symbol duration, equal to the code-length times the chip duration.

After the frequency offsets have been determined, a second order lowpass filter 82 is used to reduce noise by averaging consecutive frequency offset estimates. The filter 82 performs the following operation:

$$f_{offs,filt}(n) = \quad c_1 \cdot \left[f_{offs}(n) + f_{noise}(n)\right]$$
$$+ c_2 \cdot \left[f_{offs}(n-1) + f_{noise}(n-1) + f_{offs}(n-2) + f_{noise}(n-2) + ...\right]$$

The filter coefficients c_1 and c_2 determine the noise filter bandwidth above which the spectral noise components are suppressed.

One filtered frequency offset estimate $f_{offs,filt}(n)$ per symbol duration T_{sym} is provided to the numerically controlled oscillator 72 indicating the average phase change of the complex data stream $Z_{chip,offs}(k)$ between

5 the first and the last sample of a symbol caused by oscillator frequency offset. The oscillator 72 generates an up-sampled complex correction sequence

$$\underline{Z}_{offs}(k) = 1 \cdot \exp\{j\varphi_{offs}(k)\}$$

10

where the chip rate phase values $\varphi_{offs}(k)$ are linearly interpolated from the average phase differences per symbol duration $\Delta\varphi_{offs,filt}(n) = T_{sym} \cdot f_{offs,filt}(n)$.

15 To close the feedback loop, the multiplier 74 performs a complex multiplication, multiplying the incoming erroneous samples $Z_{chip,offs}(k)$ by the complex conjugate of the constructed correction envelope $Z_{offs}(k)$:

20

$$\underline{Z}_{chip}(k) = \underline{Z}_{chip,offs}(k) \cdot \underline{Z}_{offs}^{*}(k)$$

Initially, a frequency offset will not have been detected and hence the multiplication factor $Z_{offs}(k)$

25 at all chip sampling instances k in the first symbol is set to $(1 + j0)$ and $Z_{chip}(k)$ is equal to $Z_{chip,offs}(k)$.

The above frequency correction system does not require the presence of an extra pilot signal for

30 providing the desired oscillator frequency offset estimation and correction. Frequency offset estimation is performed using energy in the modulated and spread data symbols only. In principle, however,

such an arrangement could be applied to a pilot signal system.

It will be further understood by those skilled in the art that the foregoing description pertains to a preferred embodiment of the disclosed system and that various changes and modifications may be made in the invention without departing from the spirit and scope thereof.

We Claim

1.   A method of providing frequency correction for a
spread spectrum communication receiver, said receiver
5   being arranged to despread a code-spread signal
having a first data rate to provide at least one
despread data signal having a second, lower data
rate, wherein said method comprises the steps of:
        i) determining a frequency offset by processing
10   successive samples of said despread data signal;
        ii) generating a correction sequence from said
        determined frequency offset; and
        iii) combining said code-spread signal having
        said first data rate with said correction
15   sequence obtained from said despread data signal
        having said second, lower data rate to correct
        the determined frequency offset.

2.   The method of claim 1 further comprising the
20   step of filtering the determined frequency offset
prior to the generation of a correction sequence
therefrom to reduce noise therein.

3.   The method of claim 1 wherein said step of
25   determining a frequency offset includes the
performance of a data processing operation comprising
the calculation of the mathematical argument of a
complex sample multiplied by the complex conjugate of
a preceding complex sample.
30
4.   The method of claim 1 wherein the communication
system is a code division multiple access
communication system and wherein the frequency offset
is determined from consecutive symbol samples and the
35   frequency offset is corrected by multiplying received

data by a correction factor prior to despreading to obtain said symbol samples.

5.   The method of claim 1 wherein said correction sequence is an up-sampled complex correction sequence $Z_{offs}(k)$, where $Z_{offs}(k)$ is equal to 1 x exp $\{j\varphi_{offs}(k)\}$ where $\varphi_{offs}(k)$ represents phase offset values at the first rate which are linearly interpolated from an average phase difference at the second rate.

6.   A spread spectrum communication system comprising a plurality of receivers for receiving transmitted signals, wherein each receiver comprises:
an RF signal receiver for generating an analog signal from a received RF signal;
an analog to digital converter for converting said analog signal into a digital signal;
a digital signal despreader for processing a code-spread signal having a first data rate to obtain a despread digital signal having a second data rate, said second data rate being lower than said first data rate; and
a frequency corrector,
wherein said frequency corrector comprises a feedback loop including a frequency offset detector for obtaining a measure of a frequency offset from said despread digital signal and a frequency correction generator for generating a frequency correction and a combiner for combining said frequency correction with said code-spread signal to correct said frequency offset.

7. A spread spectrum communication system according to claim 6 wherein said feedback loop includes a filter for filtering said measure of said frequency offset to reduce noise therein.

5

8. A spread spectrum communication system according to claim 6 wherein said frequency offset detector comprises a data processor for performing a mathematical operation of determining the

10 mathematical argument of a complex sample of said despread digital signal multiplied by the complex conjugate of an immediately preceding sample of said despread digital signal.

15 9. A spread spectrum communication system according to claim 6 wherein said frequency corrector includes a multiplier for multiplying said code-spread signal by a correction factor prior to despreading said code-spread signal.

20

10. A spread spectrum communication system according to claim 6 wherein said frequency correction generator comprises an interpolator for calculating phase offset values for said code-spread signal from

25 an average phase difference calculated from samples of said despread signal.

11. A spread spectrum communication system according to claim 6 wherein said communication system is a

30 code division multiple access system.

12: A spread spectrum communication system according to claim 6 wherein said communication system is a wireless local loop link.

35

13. A receiver for a spread spectrum communication
system comprising:
an RF signal receiver for generating an analog signal
from a received RF signal;
5   an analog to digital converter for converting said
analog signal into a digital signal;
a digital signal despreader for processing a code-
spread signal having a first data rate to obtain a
despread digital signal having a second data rate,
10   said second data rate being lower than said first
data rate; and
a frequency corrector,
wherein said frequency corrector comprises a feedback
loop including a frequency offset detector for
15   obtaining a measure of a frequency offset from said
despread digital signal and a frequency correction
generator for generating a frequency correction and a
combiner for combining said frequency correction with
said code-spread signal to correct said frequency
20   offset.

ABSTRACT OF THE DISCLOSURE

A method and system for providing a frequency
correction for a spread spectrum communication
5 receiver. In the method and system, a frequency
offset is determined by processing successive samples
of a despread data signal. A correction sequence is
generated from this determined offset and combined
with a code-spread received signal prior to
10 despreading. A filter may be included in order to
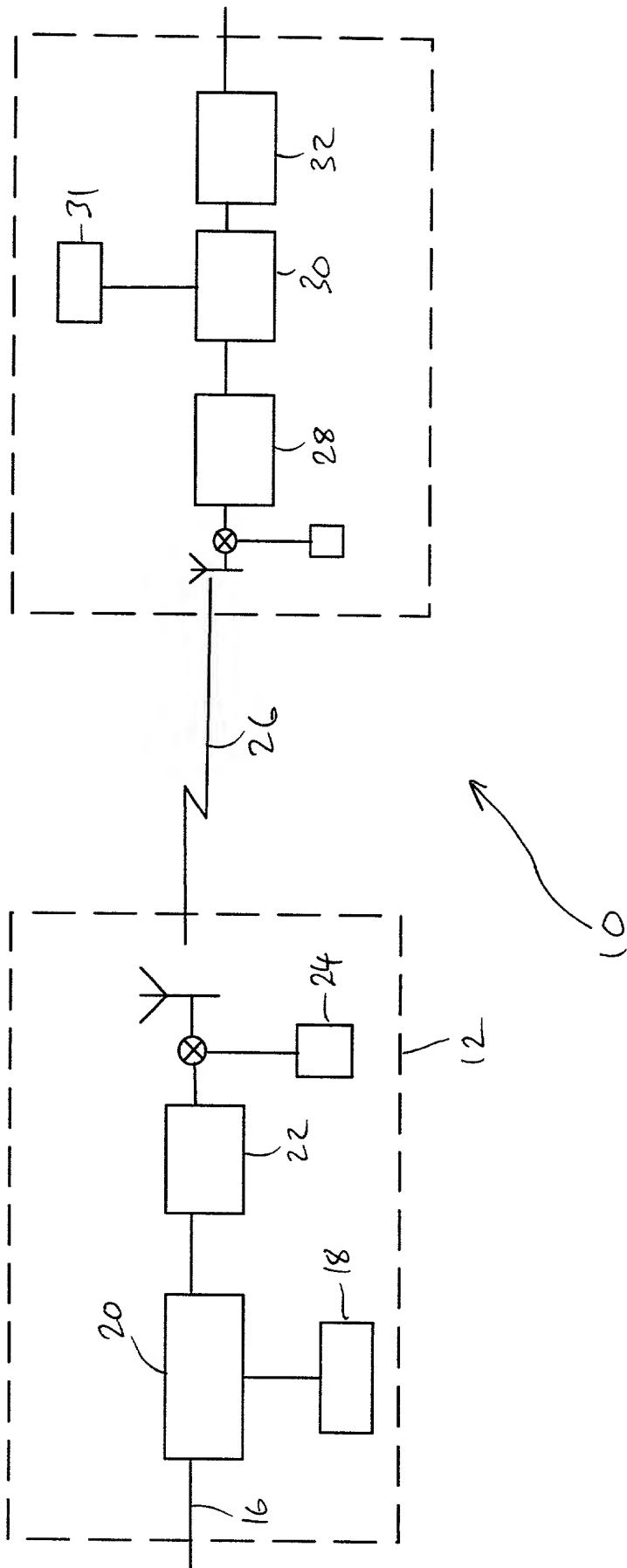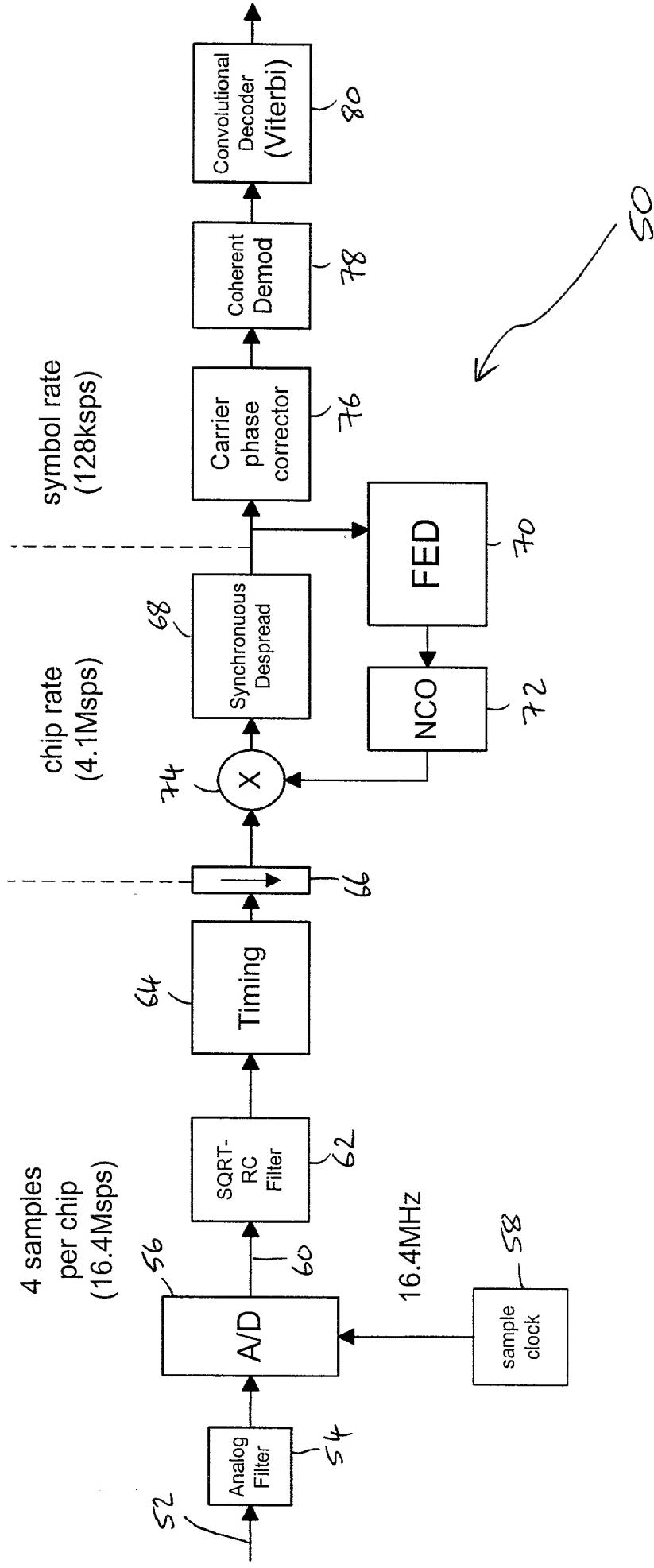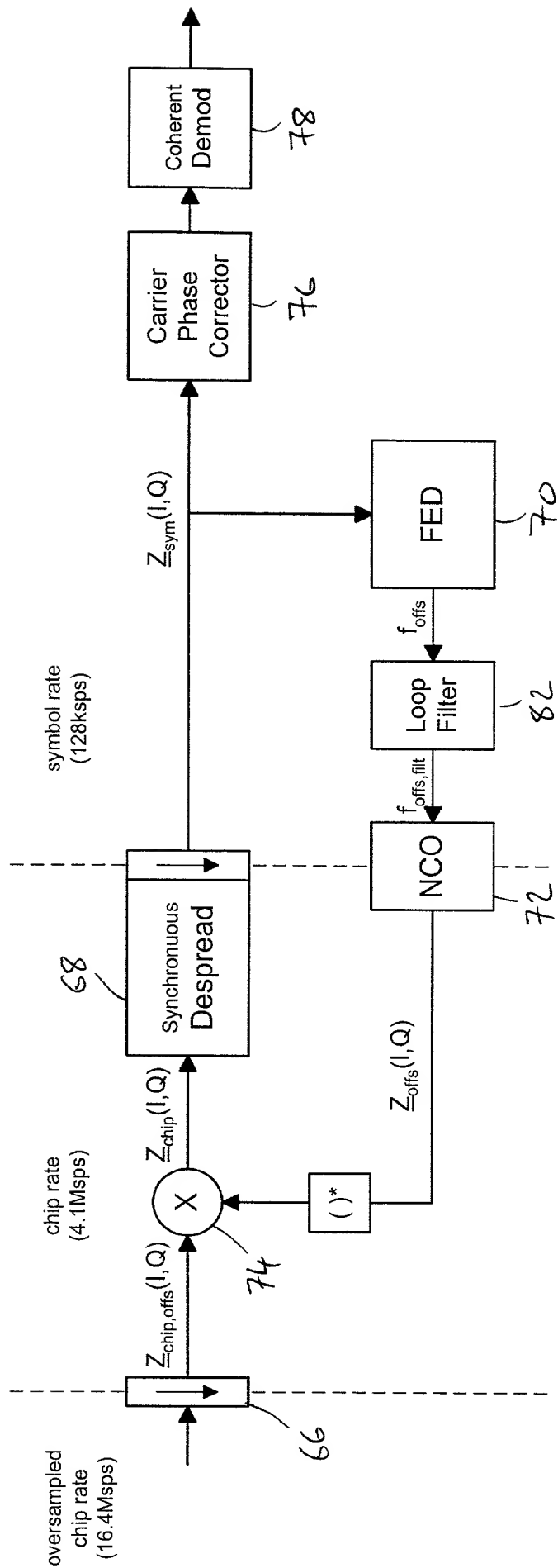reduce noise in the system.

FIG. 1

FIG. 2

FIG. 3

FORM 100 VERSION 1.0 (October 27, 1990)

DECLARATION

As a below named inventor, I hereby declare that: my residence, post office address, and citizenship are as stated below next to my name. I believe I am the original, first, and sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

DIGITAL CORRECTION METHOD AND SYSTEM

as described in the specification ■ attached or ☐ of Patent Application Serial No._____ filed_____ and amended on_____.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above; that I do not know and do not believe the same was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to this application; that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representative or assigns more than twelve months prior to this application; and that I acknowledge the duty to disclose information of which I am aware which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations § 1.56(a). Such information is material when it is not cumulative to information already of record or being made of record in the application, and

    (1) it establishes, by itself or in combination with other information, a prima facie case of unpatentability of a claim; or
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I hereby claim foreign priority benefits under Title 35, United States Code § 119 of any foreign application(s) for patent or inventor's certificates listed below and have also identified below any foreign application(s) having a filing date before that of the application(s) on which priority is claimed:

COUNTRY	APPLICATION NUMBER	DATE OF FILING	PRIORITY CLAIMED UNDER 35 USC 119
			☐YES ■NO

I hereby claim the benefit under Title 35 United States Code § 120 of any United States application(s) listed below and, insofar as any subject matter of any claim of this application is not disclosed in the prior United States Application, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations § 1.56(a) which occurred between the filing date of the prior application and the national PCT international filing date of this application:

And I (we) hereby appoint Paul R. Morico (Reg. No. 35,960), David G. Frank-Molnia (Reg. No. 42,551), Paul N. Katz (Reg. No. 35,917), Ron L. Chichester (Reg. No. 36,765), Vincenzo D. Pitruzzella (Reg. No. 28, 656), Richard J. Roddy (Reg. No. 27,688), William D. Zahrt II (Reg. No. 26,070), Paul S. Drake (Reg. No. 33,491), Louis A. Riley (Reg. No. 39,817), Elizabeth A. Apperley (Reg. No. 36,428) my attorneys with full powers of substitution and revocation, to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith.

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